# Metal Feature Set Tables



## Apple GPUs

Apple GPU Family	<b>GPUs in Family</b>	<b>Corresponding Feature Sets</b>
MTLGPUFamilyApple1	A7	iOS GPU family 1
MTLGPUFamilyApple2	A8	iOS GPU family 2, tvOS GPU family 1
MTLGPUFamilyApple3	A9, A10	iOS GPU family 3, tvOS GPU family 2
MTLGPUFamilyApple4	A11	iOS GPU family 4
MTLGPUFamilyApple5	A12	iOS GPU family 5
MTLGPUFamilyApple6	A13	_
This table lists current Apple GPU families,		
which processors are in each family, and how they relate to older feature sets.		

**Feature Availability**This table lists the availability of major Metal features.

GPU	Common	Common	Common	Apple	Apple	Apple	Apple	Apple	Apple	Мас	Мас
Family	1	2	3	1	2	3	4	5	6	1	2
GPU Family				MTLGPUFamil yApple1	MTLGPUFamil yApple2		MTLGPUFamil yApple4	MTLGPUFamil yApple5	MTLGPUFamil yApple6	MTLGPUFamil yMac1	MTLGPUFamil yMac2
Features											
MetalKit	✓	<b>✓</b>	✓	<b>✓</b>	<b>✓</b>	✓	<b>✓</b>	<b>✓</b>	✓	<b>✓</b>	<b>✓</b>
Metal Performance Shaders		<b>✓</b>	✓		✓	✓	✓	✓	✓	✓	<b>✓</b>
Programmable blending				<b>✓</b>	✓	✓	✓	<b>✓</b>	✓		
PVRTC pixel formats				<b>✓</b>	✓	✓	<b>✓</b>	<b>✓</b>	✓		
EAC/ETC pixel formats				<b>✓</b>	✓	✓	<b>✓</b>	✓	<b>✓</b>		
ASTC pixel formats					✓	✓	✓	✓	<b>✓</b>		
BC pixel formats										<b>✓</b>	✓
Compressed Volume Texture Formats		✓	✓			✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓
Extended range pixel formats						✓	<b>✓</b>	✓	<b>✓</b>		
Wide color pixel format	✓	✓	✓	<b>✓</b>	✓	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓
Depth-16 pixel format										<b>✓</b>	✓
Linear textures	✓	✓	✓	<b>✓</b>	✓	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
MSAA depth resolve			<b>✓</b>			✓	<b>✓</b>	<b>✓</b>	<b>✓</b>		<b>✓</b>
Array of textures		<b>✓</b>	<b>✓</b>			✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Cube map texture arrays		<b>✓</b>	<b>✓</b>				<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Stencil texture views	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
sRGB writes			<b>✓</b>		<b>✓</b>	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>		<b>✓</b>
Array of samplers		<b>✓</b>	<b>✓</b>			✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Sampler max anisotropy	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Sampler LOD clamp	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
MTLSamplerState support for comparison functions		<b>✓</b>	<b>✓</b>			✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
16-bit unsigned integer coordinates	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Border color										<b>✓</b>	<b>✓</b>
Counting occlusion query		<b>✓</b>	<b>✓</b>			✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Base vertex/instance drawing		<b>✓</b>	<b>✓</b>			✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Layered rendering			<b>✓</b>					<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Layered rendering to multisample textures <sup>1</sup>											<b>✓</b>
Memoryless render targets				<b>✓</b>	<b>✓</b>	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>		
Dual-source blending	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Combined MSAA store and resolve action		<b>✓</b>	✓			✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
MSAA blits	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Programmable sample positions	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Deferred store action	✓	<b>✓</b>	✓	<b>✓</b>	<b>✓</b>	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Texture barriers										<b>✓</b>	<b>✓</b>

Memory barriers										<b>✓</b>	<b>✓</b>
Tessellation		<b>✓</b>	<b>✓</b>			✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Indirect tessellation arguments								<b>✓</b>	<b>✓</b>	<b>✓</b>	✓
Tessellation in Indirect Command Buffers								<b>✓</b>	<b>✓</b>	<b>✓</b>	✓
Resource heaps	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓
Function specialization	✓	<b>✓</b>	✓								
Function buffer read-writes		<b>✓</b>	<b>✓</b>			<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓
Function texture read-writes			<b>✓</b>				<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓
Extract, insert, and reverse bits	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	✓
SIMD barrier	<b>✓</b>	<b>✓</b>									
Indirect draw & dispatch arguments		<b>✓</b>	<b>✓</b>			<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Argument buffers <sup>2</sup>	<b>✓</b>	<b>✓</b>									
Indirect command buffers		<b>✓</b>	<b>✓</b>			<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>		<b>✓</b>
Uniform type	<b>✓</b>	<b>✓</b>									
Imageblocks							<b>✓</b>	<b>✓</b>	<b>✓</b>		
Tile shaders							<b>✓</b>	<b>✓</b>	<b>✓</b>		
Imageblock sample coverage control							<b>✓</b>	<b>✓</b>	<b>✓</b>		
Post-depth coverage							<b>✓</b>	<b>✓</b>	<b>✓</b>		
Quad-scoped permute operations			<b>✓</b>				<b>✓</b>	<b>✓</b>	<b>✓</b>		<b>✓</b>
SIMD-scoped permute operations									<b>✓</b>	<b>✓</b>	<b>✓</b>
SIMD-scoped reduction operations											<b>✓</b>
Raster order groups			<b>✓</b>				<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>√</b> 3	<b>✓</b>
Non-uniform threadgroup size			<b>✓</b>				<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Multiple viewports			<b>✓</b>					<b>✓</b>	<b>✓</b>	✓	✓
Device notifications										<b>✓</b>	<b>✓</b>
Stencil feedback			<b>✓</b>					<b>✓</b>	<b>✓</b>		✓
Stencil resolve			<b>✓</b>					<b>✓</b>	<b>✓</b>		<b>✓</b>
Non-Square Tile Dispatch								<b>✓</b>	<b>✓</b>		
Texture Swizzle				<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>		<b>✓</b>
Placement Heap				<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>		<b>✓</b>
Pre-Clipped Primitive ID and Barycentric Coordinates											<b>✓</b>
Read or Write Cube Map Textures							<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>
Sparse Textures									<b>✓</b>		
Variable Rasterization Rate									<b>✓</b>		
Vertex Amplification									<b>✓</b>		
1 Available on MacOS 10 14+											

<sup>&</sup>lt;sup>1</sup> Available on MacOS 10.14+.

<sup>&</sup>lt;sup>2</sup> See <u>About Argument Buffers</u> for more information about

argument buffer tiers, limits, and capabilities.

3 Not all macOS Family 1 devices support raster order groups. You query MTLDevice rasterOrderGroupsSupported at runtime to check.

## **Implementation Limits**

This table lists the implementation limits in Metal.

GPU	Apple	Apple	Apple	Apple	Apple	Apple	Мас	Мас
Family	1	2	3	4	5	6	1	2
Feature Set	MTLGPUFamil yApple1	MTLGPUFamil yApple2	MTLGPUFamil yApple3	MTLGPUFamil yApple4	MTLGPUFamil yApple5	MTLGPUFamil yApple6	MTLGPUFamil yMac1	MTLGPUFamil yMac2
Function arguments								
Maximum number of vertex attributes, per vertex descriptor	31	31	31	31	31	31	31	31
Maximum number of entries in the buffer argument table, per graphics or compute function	31	31	31	31	31	31	31	31
Maximum number of entries in the texture argument table, per graphics or compute function	31	31	31	96	96	128	128	128
Maximum number of entries in the sampler state argument table, per graphics or compute function <sup>3</sup>	16	16	16	16	16	16	16	16
Maximum number of entries in the threadgroup memory argument table, per compute function	31	31	31	31	31	31	31	31
Maximum number of inlined constant data buffers, per graphics or compute function	31	31	31	31	31	31	14	14
Maximum length of an inlined constant data buffer, per graphics or compute function	4 KB	4 KB	4 KB					
Maximum threads per threadgroup	512	512	512	1024	1024	1024	1024	1024
Maximum total threadgroup memory allocation	16352 B	16352 B	16 KB	32 KB	32 KB	32 KB	32 KB	32 KB
Maximum total tile memory allocation <sup>4</sup>	Not accessible	Not accessible	Not accessible	32 KB	32 KB	32 KB	Not accessible	Not accessible
Threadgroup memory length alignment	16 B	16 B	16 B					
Maximum function memory allocation for a buffer in the constant address space	No limit	64 KB	64 KB					
Maximum number of inputs (scalars or vectors) to a fragment function, declared with the stage_in qualifier <sup>5</sup>	60	60	60	124	124	124	32	32
Maximum number of input components to a fragment function, declared with the stage_in qualifier <sup>5</sup>	60	60	60	124	124	124	124	124
Maximum number of function constants	65536	65536	65536	65536	65536	65536	65536	65536
Maximum tessellation factor	Not available	Not available	16	16	64	64	64	64
Maximum number of viewports and scissor rectangles, per vertex function	1	1	1	1	16	16	16	16
Maximum number of raster order groups, per fragment function	Not available	Not available	Not available	8	8	8	8	8
Resources								

Minimum buffer offset alignment	4 B	4 B	4 B	4 B	4 B	4 B	256 B	256 B
Maximum 1D texture width	8192 px	8192 px	16384 px	16384 px	16384 px	16384 px	16384 px	16384 px
Maximum 2D texture width and height	8192 px	8192 px	16384 px	16384 px	16384 px	16384 px	16384 px	16384 px
Maximum cube map texture width and height	8192 px	8192 px	16384 px	16384 px	16384 px	16384 px	16384 px	16384 px
Maximum 3D texture width, height, and depth	2048 px	2048 px	2048 px	2048 px				
Maximum number of layers per 1D texture array, 2D texture array, or 3D texture	2048	2048	2048	2048	2048	2048	2048	2048
Buffer alignment for copying an existing texture to a buffer	64 B	64 B	16 B	16 B	16 B	16 B	256 B	256 B
Render Targets								
Maximum number of color render targets per render pass descriptor	4	8	8	8	8	8	8	8
Maximum size of a point primitive	511	511	511	511	511	511	511	511
Maximum total render target size, per pixel, when using multiple color render targets	128 bits	256 bits	256 bits	512 bits	512 bits	512 bits	No limit	No limit
Maximum visibility query offset	65528 B	65528 B	256 KB	256 KB				
Synchronization								
Maximum number of fences	32768	32768	32768	32768	32768	32768	32768	32768
Maximum vertex count for vertex amplification	Not available	2	Not available	Not availabl				

<sup>&</sup>lt;sup>3</sup> Inline constant samplers, declared in Metal shading language code, also count against this limit. For example, if the feature set limit is 16, you can have 12 API samplers and 4 language samplers (16 total) but you cannot have 12 API samplers and 6 language samplers (18 total).

<sup>&</sup>lt;sup>4</sup> Tile memory can be allocated between imageblocks and threadgroup memory, but the sum of these allocations cannot exceed the maximum total tile memory limit. Some feature sets cannot access tile memory directly, but they can access threadgroup memory.

<sup>&</sup>lt;sup>5</sup> A vector counts as n scalars, where n is the number of components in the vector. In iOS and tvOS feature sets, you can only reach the maximum number of inputs if you do not exceed the maximum number of input components. For example, you can have 60 float inputs (60 input components) but you cannot have 60 float4 inputs (240 input components).

#### **Pixel Format Capabilities**

This table lists the capabilities of all Metal pixel formats. These capabilities determine the operations that can be performed on a texture that uses a given pixel format. All graphics and compute functions can read or sample from any texture, regardless of its pixel format. Additional capabilities are defined as follows:

- Filter—the texture can be filtered during sampling.
- Write—the texture can be written to by a function.6
- Color—the texture can be used as a color render target.
- Blend—the texture can be blended.
- MSAA—the texture can be used as the destination for multisample antialias (MSAA) data.
- Resolve—the texture can be used as the destination for resolved MSAA data.
- All—the texture has all the previously-listed capabilities.

GPU	Apple	Apple	Apple	Apple	Apple	Apple	Мас	Mac
Family	1	2	3	4	5	6	1	2
Feature Set	MTLGPUFamil yApple1	MTLGPUFamil yApple2	MTLGPUFamil yApple3	MTLGPUFamil yApple4	MTLGPUFamil yApple5	MTLGPUFamil yApple6	MTLGPUFamil yMac1	MTLGPUFamil yMac2
Ordinary 8-bit pixel formats								
A8Unorm	Filter	Filter	Filter	Filter	Filter	Filter	Filter	Filter
R8Unorm <sup>8</sup>	All	All	All	All	All	All	All	All
R8Unorm_sRGB	Filter Color MSAA Resolve Blend	All	All	All	All	All	Not available	Not available
R8Snorm	Filter Write Color MSAA Blend	All	All	All	All	All	All	All
R8Uint <sup>8</sup> R8Sint	Write Color MSAA	Write Color MSAA	Write Color MSAA	Write Color MSAA	Write Color MSAA	Write Color MSAA	Write Color MSAA	Write Color MSAA
Ordinary 16-bit pixel formats								
R16Unorm R16Snorm	Filter Write Color MSAA Blend	Filter Write Color MSAA Blend	Filter Write Color MSAA Blend	Filter Write Color MSAA Blend	Filter Write Color MSAA Blend	Filter Write Color MSAA Blend	All	All
R16Uint <sup>8</sup> R16Sint <sup>8</sup>	Write Color MSAA	Write Color MSAA	Write Color MSAA	Write Color MSAA	Write Color MSAA	Write Color MSAA	Write Color MSAA	Write Color MSAA
R16Float <sup>8</sup>	All	All	All	All	All	All	All	All

RG8Unorm	All							
RG8Unorm_sRGB	Filter Color MSAA Resolve Blend	All	All	All	All	All	Not available	Not available
RG8Snorm	Filter Write Color MSAA Blend	All						
RG8Uint RG8Sint	Write Color MSAA							
Packed 16-bit pixel formats								
B5G6R5Unorm A1BGR5Unorm ABGR4Unorm BGR5A1Unorm	Filter Color MSAA Resolve Blend	Filter Color MSAA Resolve Blend	Filter Color MSAA Resolve Blend	Filter Color MSAA Resolve Blend	Filter Color MSAA Resolve Blend	Filter Color MSAA Resolve Blend	Not available	Not available
Ordinary 32-bit pixel formats								
R32Uint <sup>8</sup> R32Sint <sup>8</sup>	Write Color	Write Color	Write Color	Write Color	Write Color	Write Color	Write Color MSAA	Write Color MSAA
R32Float <sup>8</sup>	Write Color MSAA Blend	Write Color MSAA Blend	Write Color MSAA Blend	Write Color MSAA Blend	Write Color MSAA Blend	Write Color MSAA Blend	All	All
RG16Unorm RG16Snorm	Filter Write Color MSAA Blend	Filter Write Color MSAA Blend	Filter Write Color MSAA Blend	Filter Write Color MSAA Blend	Filter Write Color MSAA Blend	Filter Write Color MSAA Blend	All	All
RG16Uint RG16Sint	Write Color MSAA							
RG16Float	All							
RGBA8Unorm	All							
RGBA8Unorm_sRGB	Filter Color MSAA Resolve Blend	All	All	All	All	All	Filter Color MSAA Resolve Blend	Filter Color MSAA Resolve Blend

RGBA8Snorm	Filter	All	All	All	All	All	All	All
	Write Color							
	MSAA							
	Blend							
RGBA8Uint <sup>8</sup>	Write	Write	Write	Write	Write	Write	Write	Write
RGBA8Sint <sup>8</sup>	Color MSAA	Color MSAA	Color MSAA	Color MSAA	Color MSAA	Color MSAA	Color MSAA	Color MSAA
BGRA8Unorm <sup>8</sup>	All	All	All	All	All	All	All	All
BGRA8Unorm_sRGB	Filter	All	All	All	All	All	Filter	Filter
BGRASOHOHII_SRGB	Color	7 (1)	,	7	7	7	Color	Color
	MSAA						MSAA	MSAA
	Resolve						Resolve	Resolve
	Blend						Blend	Blend
Packed 32-bit pixel formats								
RGB10A2Unorm	Filter	Filter	All	All	All	All	All	All
	Color MSAA	Color MSAA						
	Resolve	Resolve						
	Blend	Blend						
RGB10A2Uint	Color	Color	Write	Write	Write	Write	Write	Write
	MSAA	MSAA	Color	Color	Color	Color	Color	Color
			MSAA	MSAA	MSAA	MSAA	MSAA	MSAA
RG11B10Float	Filter	Filter	All	All	All	All	All	All
	Color MSAA	Color MSAA						
	Resolve	Resolve						
	Blend	Blend						
RGB9E5Float	Filter	Filter	All	All	All	All	Filter	Filter
	Color	Color						
	MSAA	MSAA						
	Resolve Blend	Resolve Blend						
Ordinary 64-bit pixel formats	Biolid	Bieria						
	Write	Write	Write	Write	Write	Write	Write	Write
RG32Uint RG32Sint	Color	Color	Color	Color	Color	Color	Color	Color
NOSZOIIIL							MSAA	MSAA
RG32Float	Write	Write	Write	Write	Write	Write	All	All
	Color	Color	Color	Color	Color	Color		
	Blend	Blend	Blend	Blend	Blend	Blend		
RGBA16Unorm	Filter	Filter	Filter	Filter	Filter	Filter	All	All
RGBA16Snorm	Write Color	Write Color	Write Color	Write Color	Write Color	Write Color		
	MSAA	MSAA	MSAA	MSAA	MSAA	MSAA		
	Blend	Blend	Blend	Blend	Blend	Blend		

RGBA16Uint8	Write	Write	Write	Write	Write	Write	Write	Write
RGBA16Sint <sup>8</sup>	Color MSAA	Color MSAA	Color MSAA	Color MSAA	Color MSAA	Color MSAA	Color MSAA	Color MSAA
RGBA16Float <sup>8</sup>	All	All	All	All	All	All	All	All
Ordinary 128-bit pixel formats								
RGBA32Uint <sup>8</sup> RGBA32Sint <sup>8</sup>	Write Color	Write Color	Write Color	Write Color	Write Color	Write Color	Write Color MSAA	Write Color MSAA
RGBA32Float <sup>8</sup>	Write Color	Write Color	Write Color	Write Color	Write Color	Write Color	All	All
Compressed pixel formats								
PVRTC pixel formats <sup>7</sup>	Filter	Filter	Filter	Filter	Filter	Filter	Not available	Not available
EAC/ETC pixel formats	Filter	Filter	Filter	Filter	Filter	Filter	Not available	Not available
ASTC pixel formats	Not available	Filter	Filter	Filter	Filter	Filter	Not available	Not available
HDR ASTC pixel formats	Not available	Not available	Not available	Not available	Not available	Filter	Not available	Not available
BC pixel formats	Not available	Not available	Not available	Not available	Not available	Not available	Filter	Filter
YUV pixel formats								
GBGR422 BGRG422	Filter	Filter	Filter	Filter	Filter	Filter	Filter	Filter
Depth and stencil pixel formats								
Depth16Unorm	Filter MSAA	Filter MSAA	Filter MSAA Resolve	Filter MSAA Resolve	Filter MSAA Resolve	Filter MSAA Resolve	Filter MSAA Resolve	Filter MSAA Resolve
Depth32Float	MSAA	MSAA	MSAA Resolve	MSAA Resolve	MSAA Resolve	MSAA Resolve	Filter MSAA Resolve	Filter MSAA Resolve
Stencil8	MSAA	MSAA	MSAA	MSAA	MSAA	MSAA	MSAA	MSAA
Depth24Unorm_Stencil88	Not available	Not available	Not available	Not available	Not available	Not available	Filter MSAA Resolve	Filter MSAA Resolve
Depth32Float_Stencil8	MSAA	MSAA	MSAA Resolve	MSAA Resolve	MSAA Resolve	MSAA Resolve	Filter MSAA Resolve	Filter MSAA Resolve
X24_Stencil8	Not available	Not available	Not available	Not available	Not available	Not available	MSAA	MSAA
X32_Stencil8	MSAA	MSAA	MSAA	MSAA	MSAA	MSAA	MSAA	MSAA
Extended range and wide color pixel form	ats							
BGRA10_XR BGRA10_XR_sRGB BGR10_XR BGR10_XR_sRGB	Not available	Not available	All	All	All	All	Not available	Not available

BGR10A2Unorm	All	All	All	All	All	All	Filter	Filter
							Color	Write
							MSAA	Color
							Resolve	MSAA
							Blend	Resolve
								Blend

<sup>&</sup>lt;sup>6</sup> Read-write textures are available in some feature sets, where the texture can be both read from and written to by the same function. Query the readWriteTextureSupport property of MTLDevice to determine support.

<sup>7</sup> For PVRTC pixel formats, the clamp\_to\_zero sampler state is supported only in the iOS GPU Family 3 and 4 feature sets.

<sup>8</sup> Support for this pixel format varies by device. Query the depth24Stencil8PixelFormatSupported property of MTLDevice to determine support.

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